



Intern: Device Modeling and Design

Job Description

The individual selected for this internship position will be working closely with the project leader to develop semiconductor device models in Python environment. The individual will have the opportunity to learn semiconductor device physics, math modeling and finite element analysis.

Key Responsibilities (one or more of the following, depending on individual interest and strength)

- Build compact model for devices with Python or other open source software.
- Using the result from compact modeling for circuit simulations.
- Learn and simulate semiconductor devices using open-source TCAD software.

Required Qualifications

- Undergrad or graduate student in Computer Science, Mathematics, Physics, Electrical Engineering, Materials Science, or related areas.
- Strong sense of urgency; Self-motivated. Possessing initiative and drive to work around obstacles.
- Can commit 10 hr/week.

Preferred Qualifications

- Skilled in programming and math.
- Knowledge in semiconductor physics and partial differential equations.
- Experience with Linux, Python or other open-source software.
- The spirit of hacker and fast learner.

Salary

- Starting at \$25/hr, depending on skill levels.

About the Company

Cambridge Electronics, Inc. (CEI) is an MIT spinoff that is developing next generation GaN semiconductor devices to revolutionize the future of power electronics and communication. CEI offers a dynamic environment, great potential for personal growth and competitive compensation packages. CEI is an equal opportunity employer. All qualified applicants will receive consideration for employment without regard to race, color, religion, gender, gender identity or expression, sexual orientation, national origin, genetics, disability, age, or veteran status.

Contact: Dr. Harbing Lou (harbing@gantechnology.com)